

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

**INFORMATION DISCLOSURE STATEMENT**

Docket Number

**2885/98**Application Number  
To Be Assigned**10/561135**Filing Date  
HerewithExaminer  
To Be AssignedArt Unit  
To Be AssignedTitle  
**DATA PROCESSING DEVICE AND METHOD**Applicant(s)  
**Martin Vorbach et al.**

Address to:  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

1. In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and in conformance with the procedures of 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609, attorneys for Applicant(s) hereby bring the following reference(s) to the attention of the Examiner. The reference(s) are listed on the attached modified PTO Form No. 1449. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.
2. The filing of this Information Disclosure Statement and the attached PTO Form No. 1449, shall not be construed as an admission that the information cited is prior art, or is considered to be material to patentability as defined in 37 C.F.R. § 1.56(b).
3. A copy of each patent, publication or other information listed on the modified PTO form 1449 is enclosed, except as otherwise indicated on the modified PTO form 1449.

Dated: 15 Dec 2005By: 

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<b>INFORMATION DISCLOSURE          STATEMENT BY APPLICANTS          PTO FORM 1449</b>	ATTY. DOCKET NO. <b>2885/98</b>	SERIAL NO. <b>107581135</b> Not yet assigned
	APPLICANT(s) <b>Martin Vorbach et al.</b>	
	FILING DATE Herewith	GROUP Not yet assigned

**U. S. PATENT DOCUMENTS**

EXAMINER INITIAL	PATENT/PUBLICATION NUMBER	PATENT/PUBLICATION DATE	NAME	CLASS	SUBCLASS	FILING DATE
	*2003/056202	March 20, 2003	Frank et al.			
	*2002/083308	June 27, 2002	Pereira et al.			
	*6,282,627	August 28, 2001	Dale et al.			

\*Copy of reference is not enclosed because reference is cited and described in Search Report (copy of reference provided by International Searching Authority).

**FOREIGN PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
	WO 92/01987	February 6, 1992	PCT			X*	

\*Copy of reference is not enclosed because reference is cited and described in Search Report (copy of reference provided by International Searching Authority).

**OTHER DOCUMENTS**

EXAMINER INITIAL	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
	SIEMERS ET AL., "THE >S<PUTER: A NOVEL MICROARCHITECTURE MODEL FOR EXECUTION INSIDE SUPERSCALAR AND VLIW PROCESSORS USING RECONFIGURABLE HARDWARE", 1998, PAGES 169-178, COMPUTER ARCHITECTURE. PROCEEDINGS OF THE AUSTRALASIAN COMPUTER ARCHITECTURE CONFERENCE
	HAUSER J R ET AL., "GARP: A MIPS PROCESSOR WITH A RECONFIGURABLE COPROCESSOR", APRIL 16, 1997, PAGES 12-21 FIELD-PROGRAMMABLE CUSTOM COMPUTING MACHINES, PROCEEDINGS., THE 5 <sup>TH</sup> ANNUAL IEEE SYMPOSIUM ON NAPA VALLEY, CA, USA
	WITTIG R D ET AL. "ONECHIP: AN FPGA PROCESSOR WITH RECONFIGURABLE LOGIC" APRIL 17, 1996, PAGES 126-135, FPGAS FOR CUSTOM COMPUTER MACHINES, 1996. PROCEEDINGS. IEEE SYMPOSIUM ON NAPA VALLEY, CA, LOA ALAMITOS, CA USA, IEEE COMPUT. SOC., US
	KASTRUP B, "AUTOMATIC HARDWARE SYNTHESIS FOR A HYBRID RECONFIGURABLE CPU FEATURING PHILIPS CPLDS", 1998, PAGES 5-10, PROCEEDINGS OF THE PACT WORKSHOP ON RECONFIGURABLE COMPUTING, XX, XX
	RAZDAN R ET AL., "A HIGH-PERFORMANCE MICROARCHITECTURE WITH HARDWARE PROGRAMMABLE FUNCTIONAL UNITS," NOVEMBER 30, 1994, PAGES 172-180, PROCEEDINGS OF THE ANNUAL INTERNATIONAL SYMPOSIUM ON MICROARCHITECTURE, XX, XX

EXAMINER	DATE CONSIDERED
EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	